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Sheet 1 of 1

Application No. :

Filing Date :

First Named Inventor: H. IIZUKA

Group Art Unit :

Examiner Name :

Attorney Docket No. : FUJI 20.747

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No. ¹	U.S. Patent Document	Kind Code if known ²	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear

FOREIGN DOCUMENTS

Examiner Initials	Cite No. ¹	Foreign Patent Document Office ³ Number ⁴ Kind Code ⁵ (if known)	Country	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns Lines Where Relevant Passages or Relevant Figures Appear
AMD		11-122192	JP	Fujitsu Ltd.	04-30-1999	

Other Prior Art-Non Patent Literature Documents

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, country, where published, source.	Applicant check her if English language translation attached
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AMD		European Search Report dated March 10, 2005	
		H. SUZUKI ET AL. Dynamic Gain Control by Maximum Signal Power Channel in Optical Linear Repeaters for WDM Photonic Transport Networks. IEEE Photonics Technology Letters, IEE Inc. New York, U.S. vol.10, no.5, May 1, 1998, pages 734-736	
		S.Y. PARK ET AL. A gain-flattened two-stage EDFA for WDM optical networks with a fast link control channel. Optics Communications, Vol. 153, no.1-3, July 15, 1998 pages 23-26	
Examiner Signature	/Ari M. Diacou/		Date Considered 02/15/2007

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